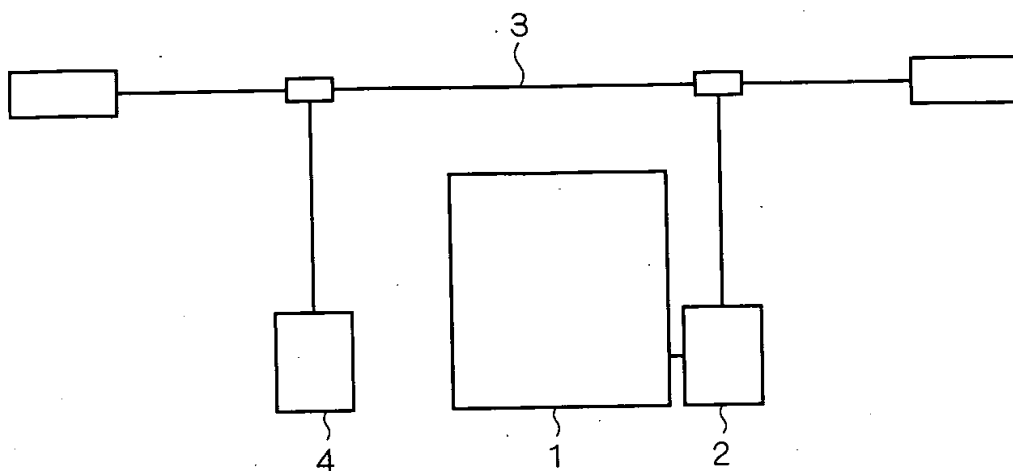
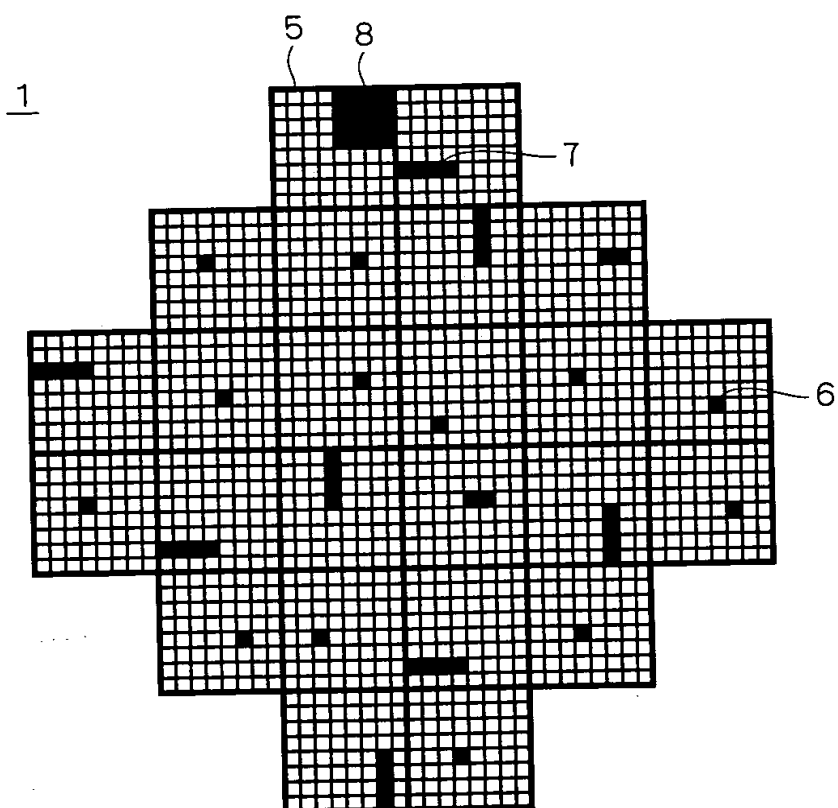


F I G . 1

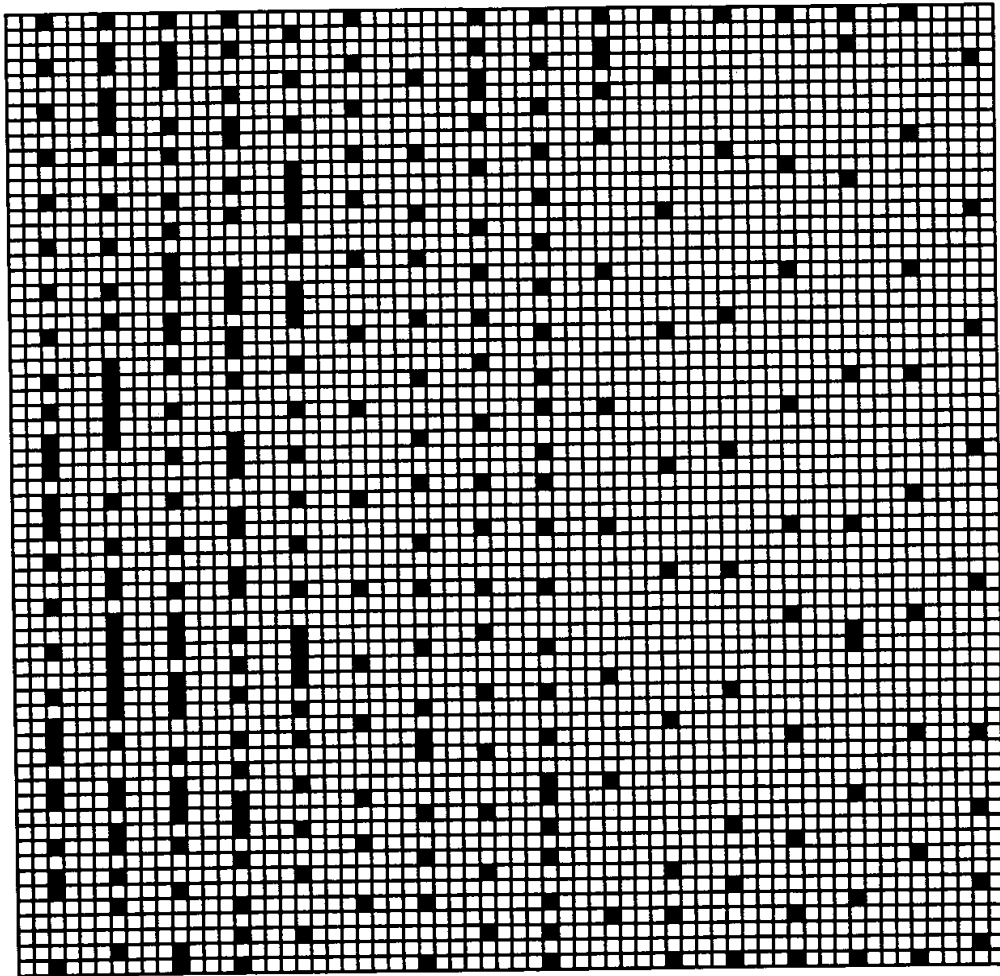


F I G . 2

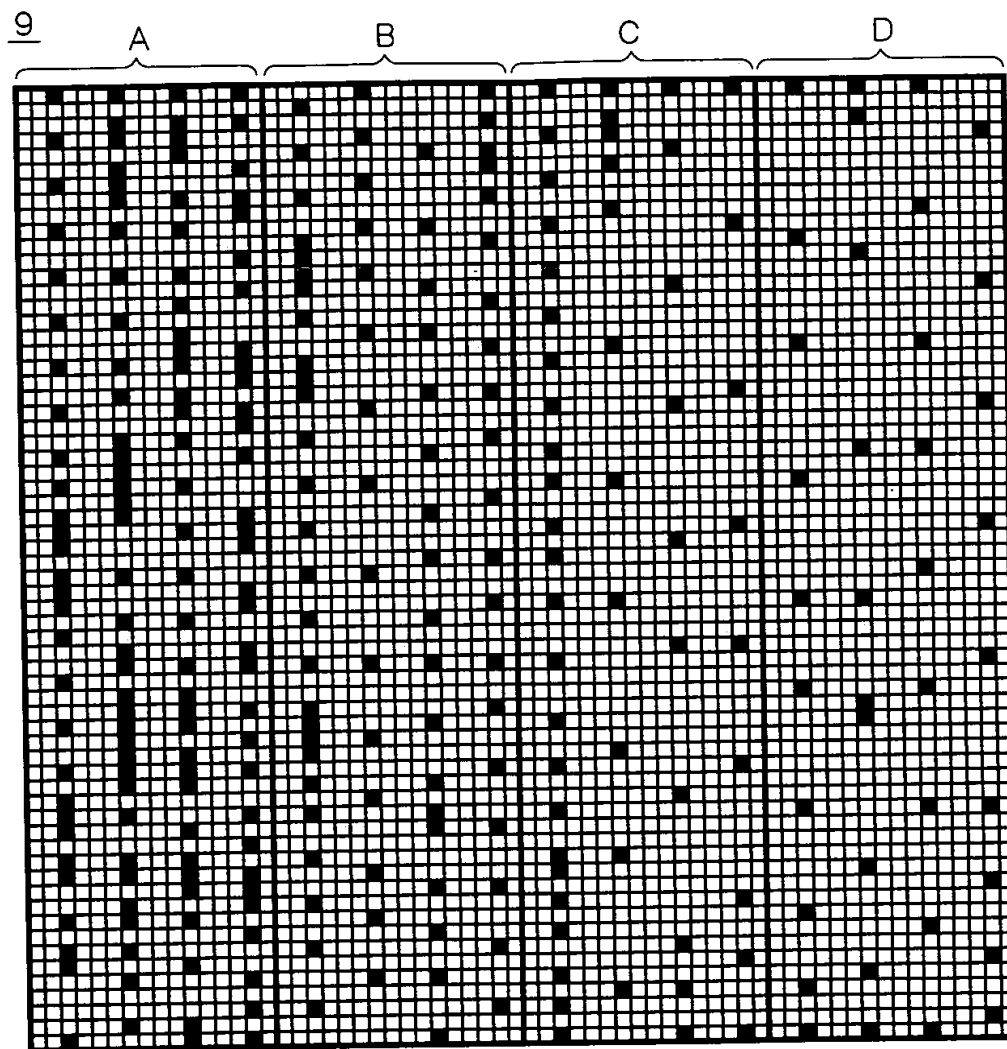


F I G . 3

9



F I G . 4 A

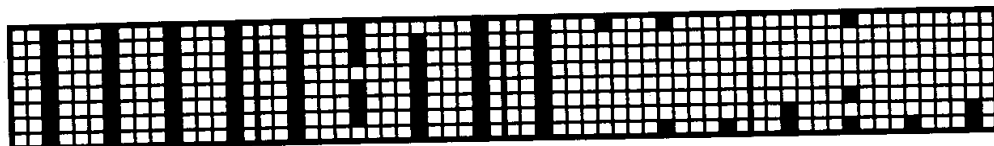


F I G . 4 B

COLUMN	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
NUMBER OF FAILURE BITS	0	0	90	0	0	0	72	0	0	0	70	0	0	0	70	0

F I G . 5 A

10

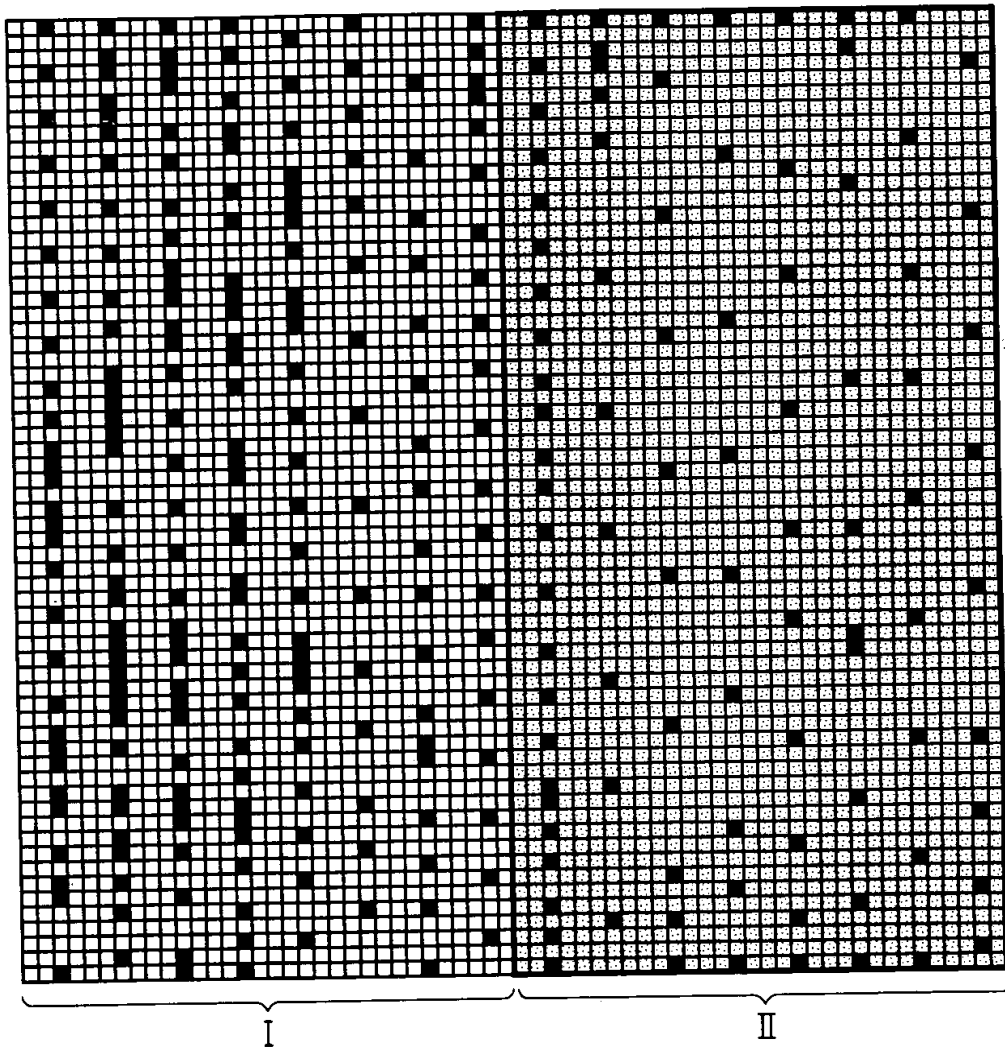


F I G . 5 B

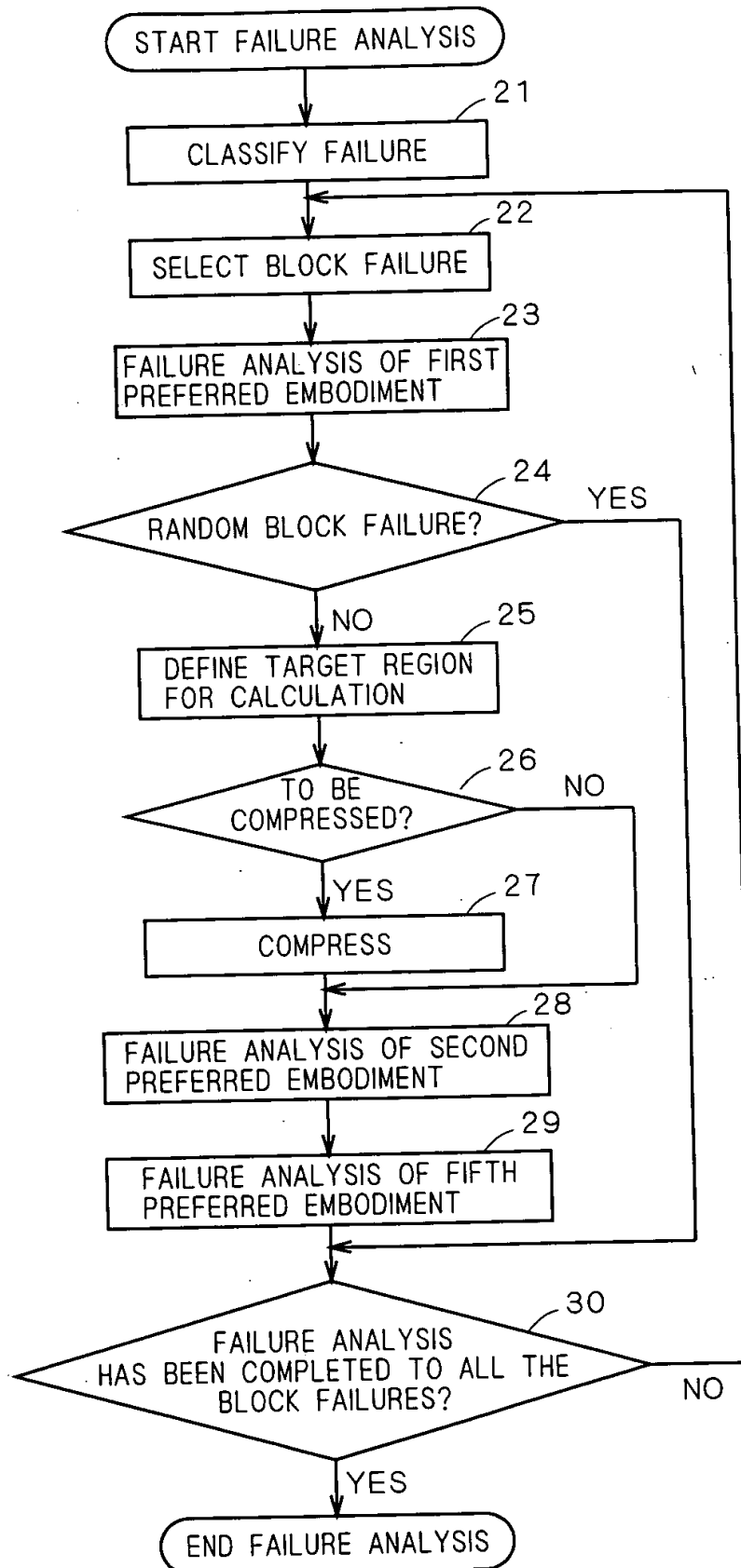
COLUMN	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
NUMBER OF FAILURE BITS	0	0	26	0	0	0	18	0	0	0	18	0	0	0	19	0

F I G . 6

9

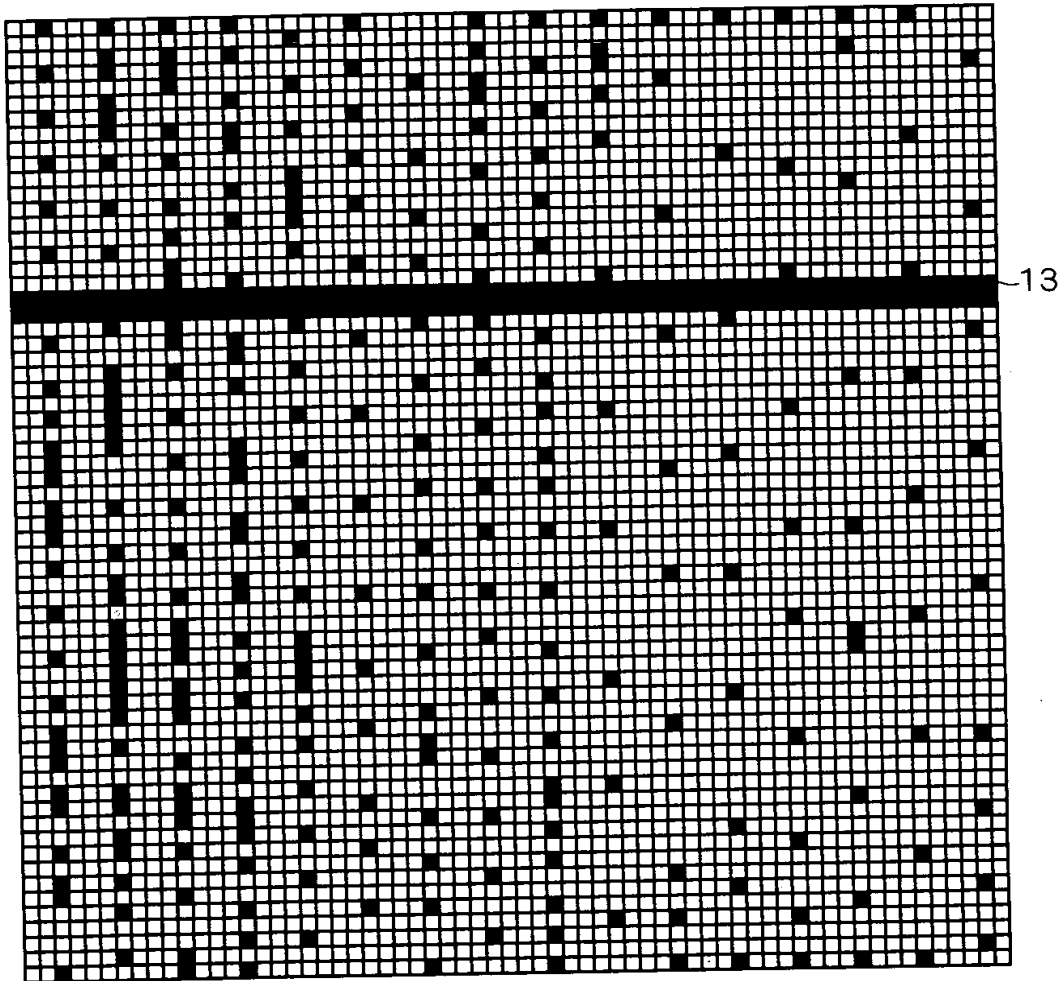


F I G . 7



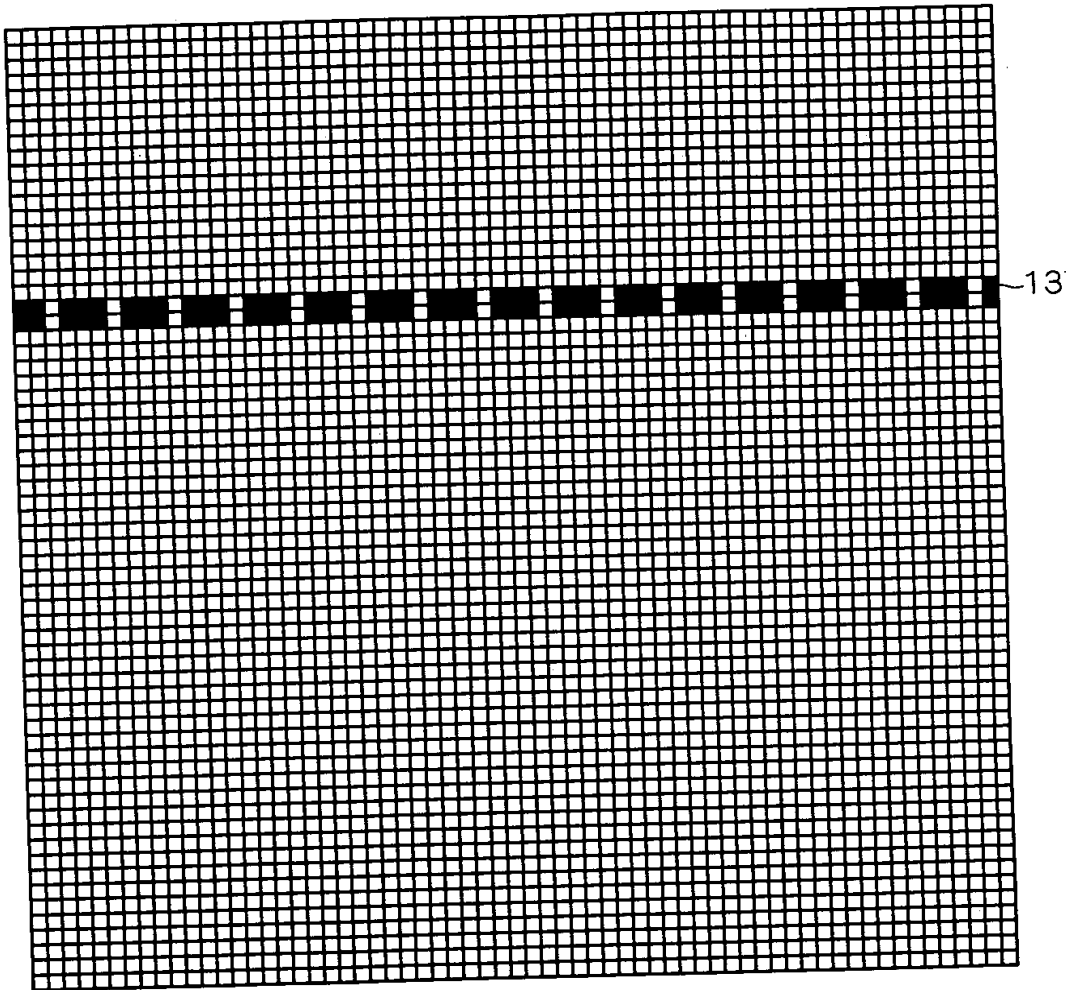
F I G . 8

11



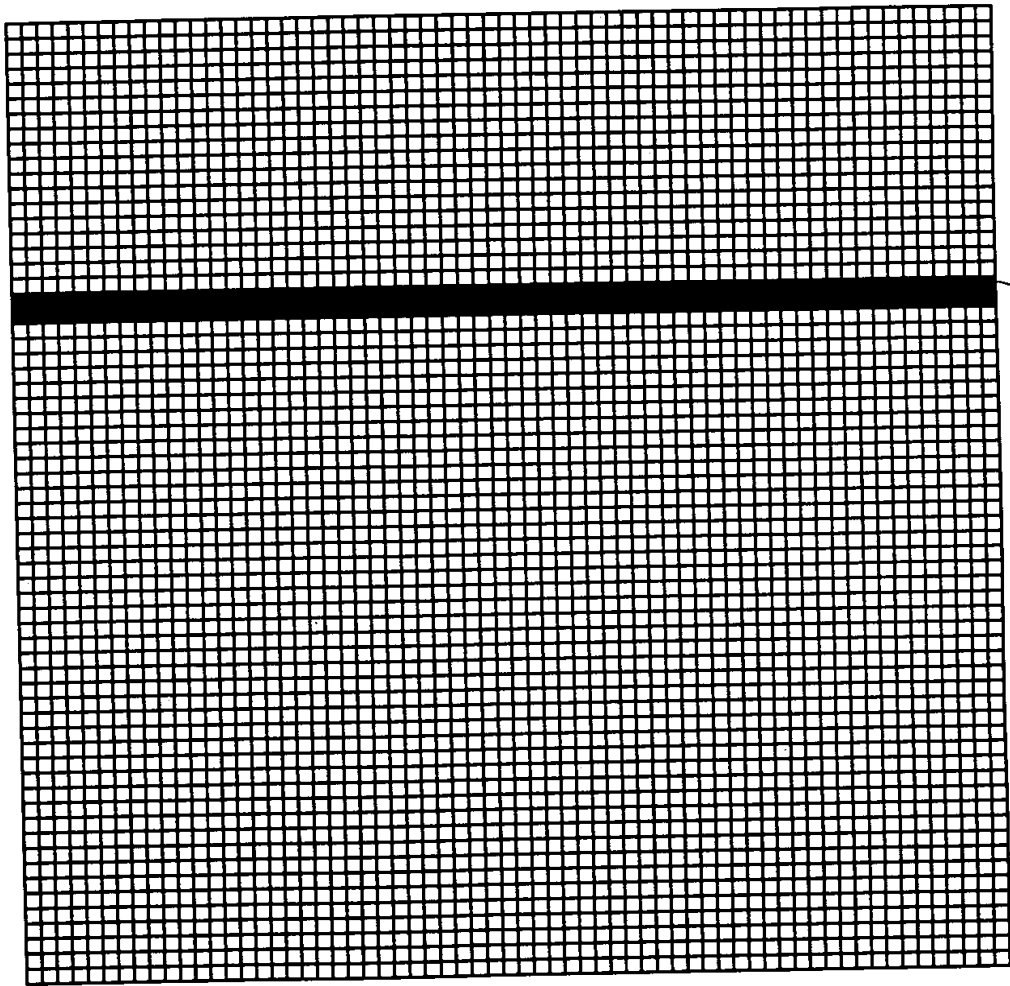
F I G . 9

12



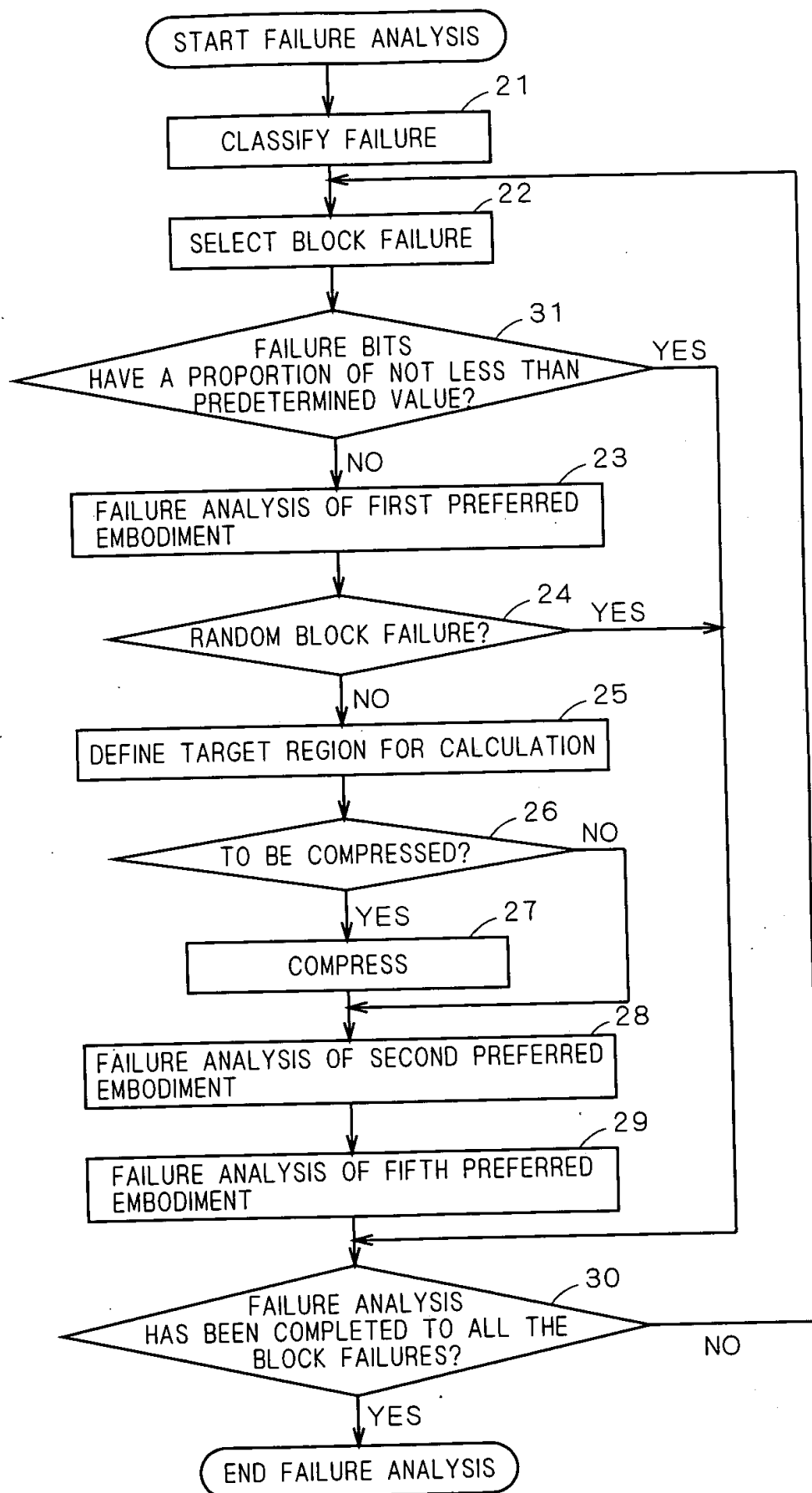
F I G . 1 0

14



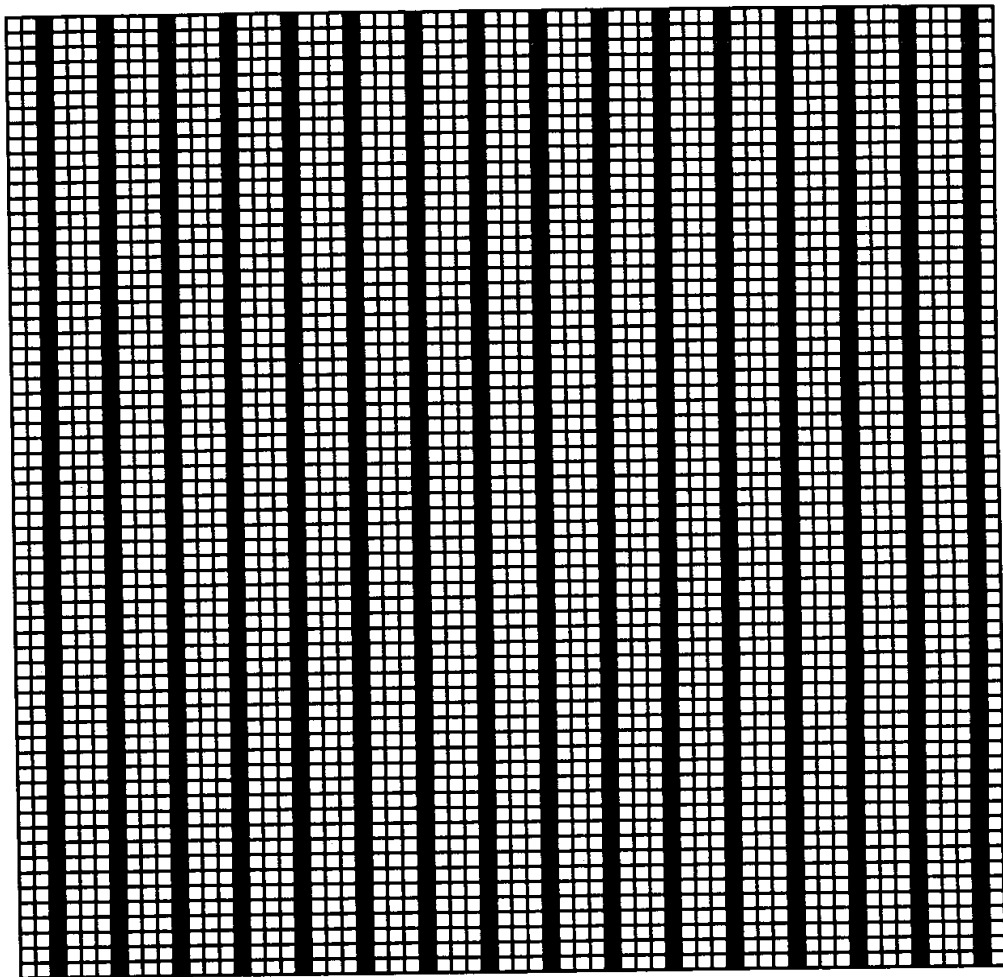
15

FIG. 11



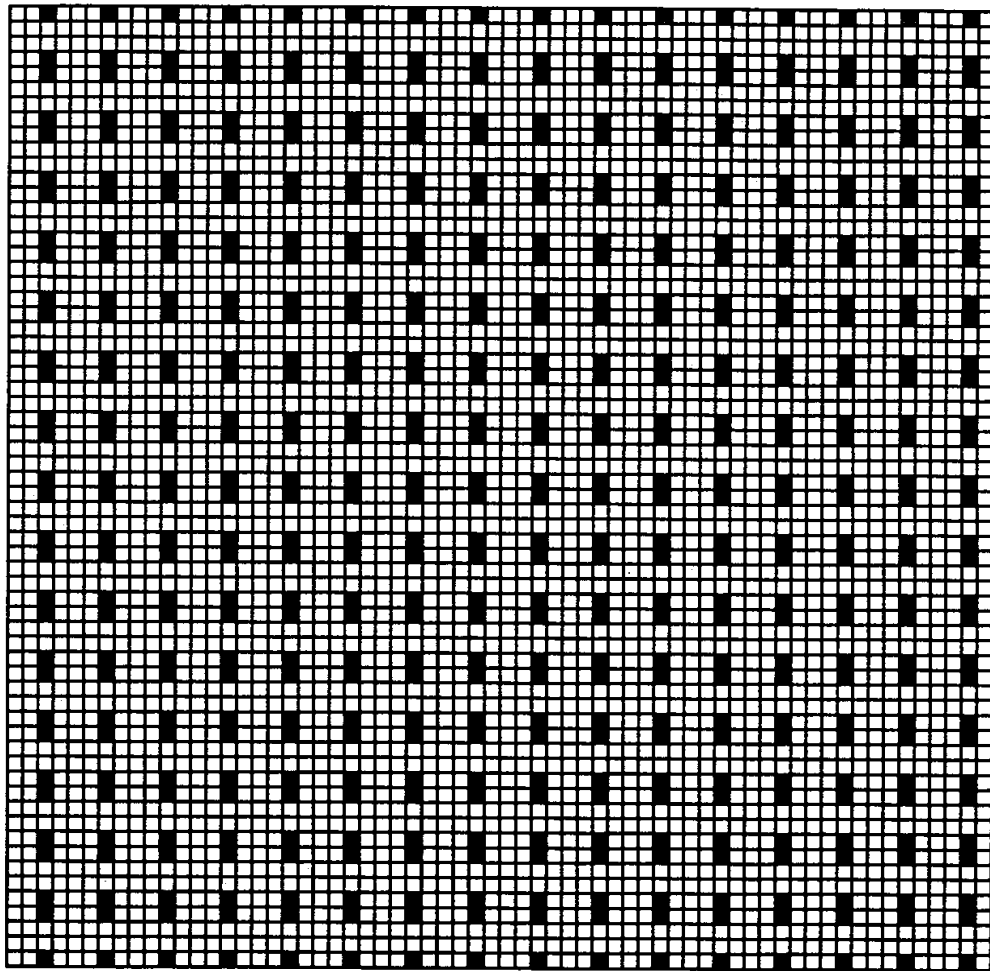
F I G . 1 2

16



F I G . 1 3

17



F I G . 1 4

18

